

Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER	
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U.S. UTILITY Patent Application

I.P.E.

PATENT DATE

SCANNED BY

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/902673	D F	438	745	2825	L. MACSHANAHAN
BEST AVAILABLE COPY Method of manufacturing a semiconductor integrated circuit device					

PTO-2040
1269

ISSUING CLASSIFICATION

ORIGINAL

CROSS REFERENCES

SEARCHED INDEXED

CLASS

SUBCLASSES (ONE SUBCLASS PER BLOCK)

EXAMINER'S CLASSIFICATION

SEARCHED

INDEXED

CLASSIFIED

DRAWINGS

Search Copy.

Fig. Draw.

Print Fig.

CLAIMS ALLOWED

Total claims

Claims allowed

SEARCHED

INDEXED

CLASSIFIED

FILED WITH: CLEA RCE DOL CDR CDR-C
(check all that apply)

(PAGE)